

CLOSING THE POWER GAP BETWEEN ASIC AND CUSTOM%0A

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Using these methods, we believe that the power gap between ASICs and custom circuits can be closed to within 2.

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contribute to the gap between ASIC and custom power. Table 4 outlines our analysis of the most significant design factors and their impact on the total power when a chip is active. The typical column shows the maximum contribution of the factors. In total these factors can make power an order of magnitude worse.

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By using high performance and low power techniques that can be integrated within an EDA methodology, we believe that the power gap between ASIC and custom designs may be closed to within 2.6x.

Closing the Gap Between ASIC and Custom: An ASIC Perspective

Closing the Gap Between ASIC and consumes 6.3W of power[21]. ASIC microprocessors are

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This new book from the authors of "Closing the Gap between ASIC & Custom" was published at just the right time. It follows a similar format as the previous book, but instead discusses the considerations involved in minimizing power consumption of processors.